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FIG. 3

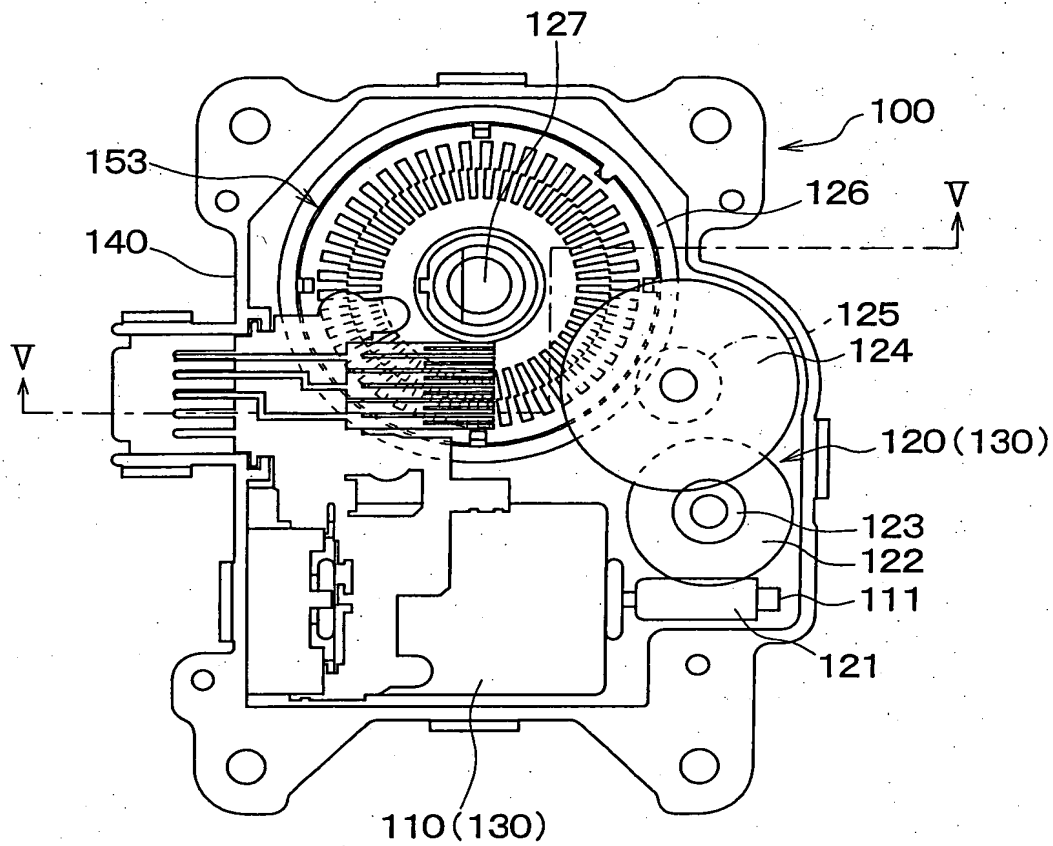


FIG. 4A

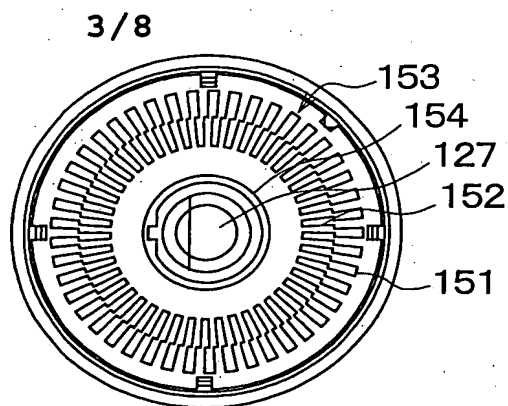


FIG. 4B

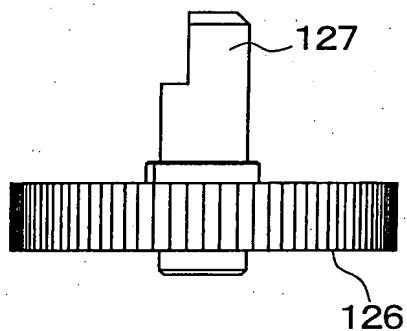
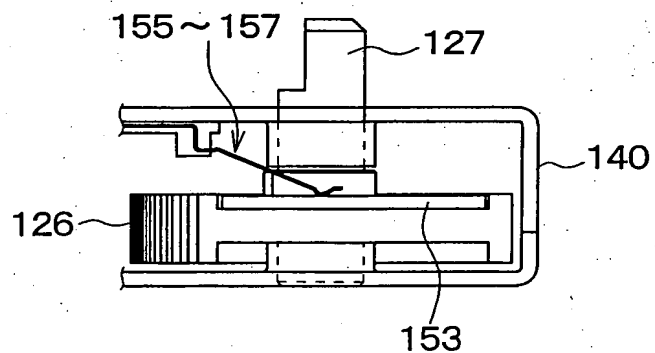


FIG. 5



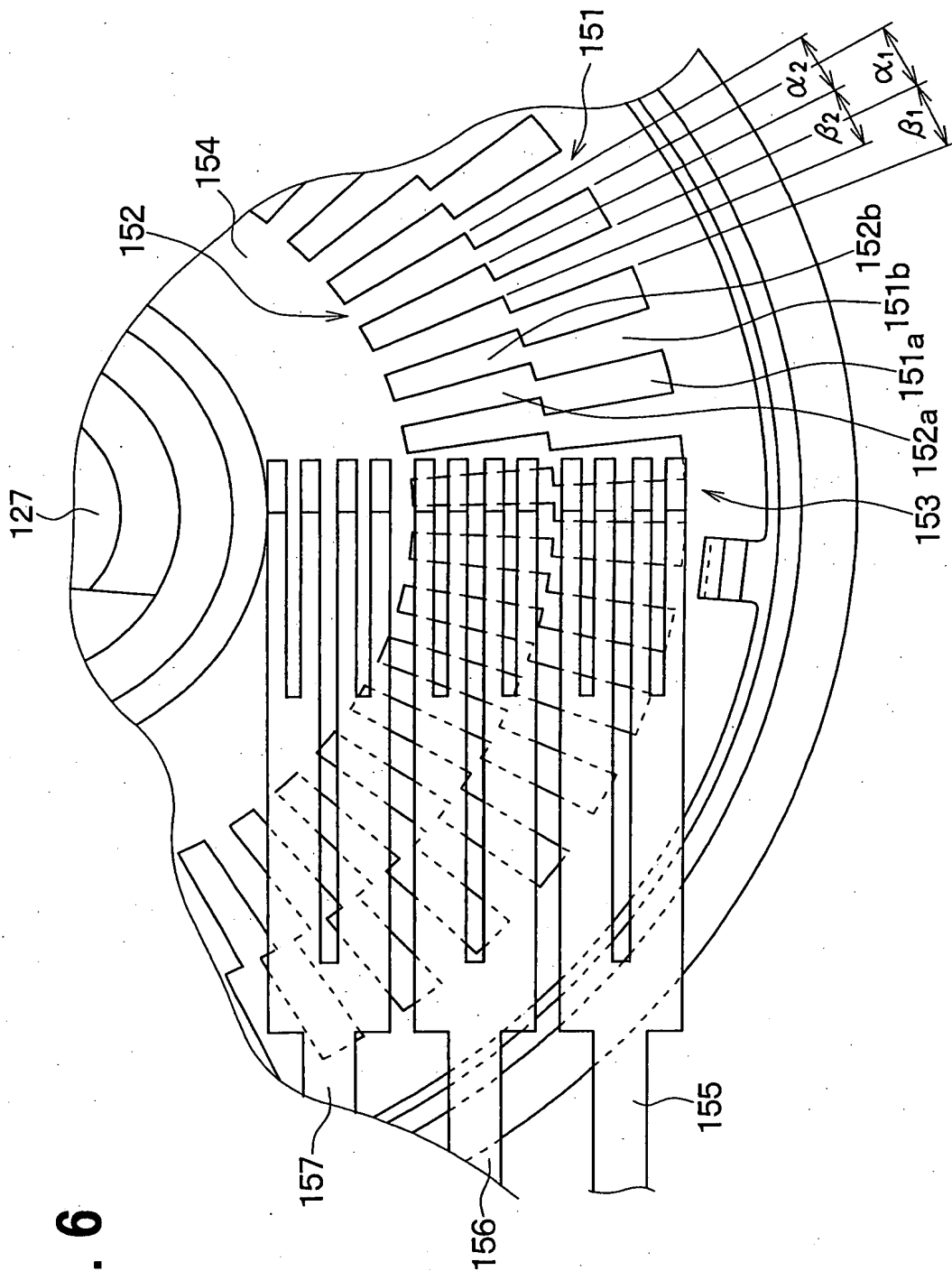
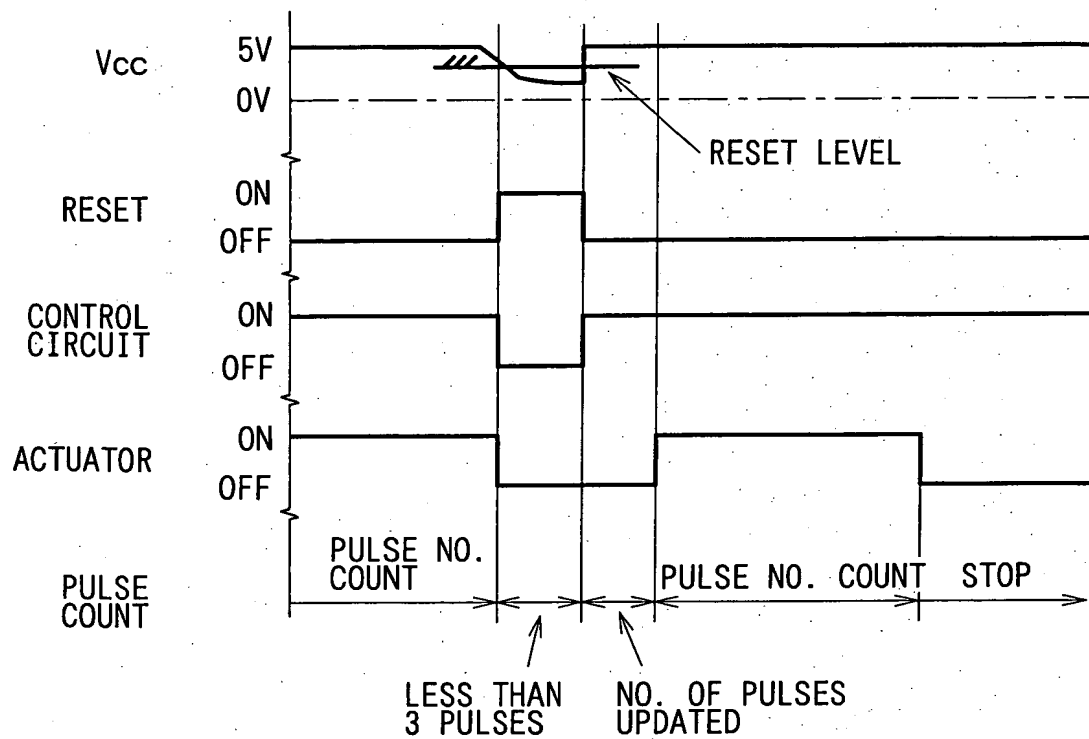


FIG. 6

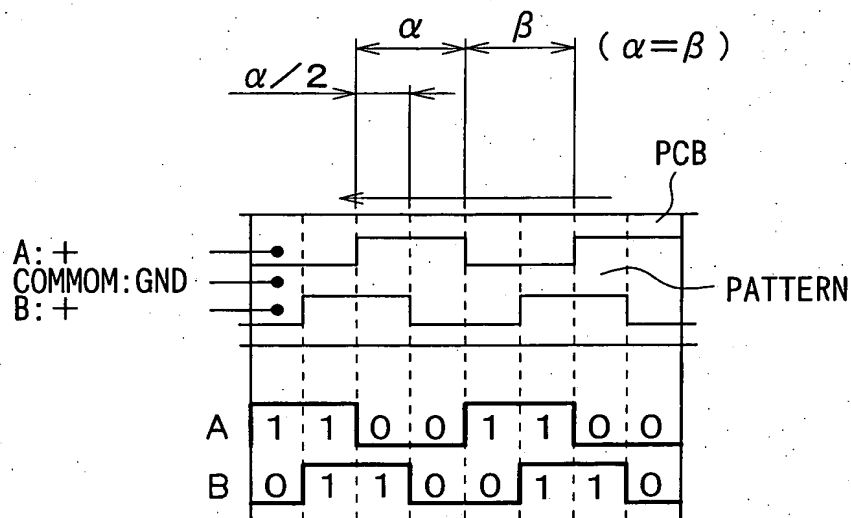
The diagram shows a control system 200. It includes a CPU 270 which contains a MOTOR CONTROL 220, MEMORY 260, PULSE COUNTER 230, and RAM 230. A MOTOR DRIVE 250 is connected to the MOTOR CONTROL 220 and a MOTOR 110. A CONSTANT VOLTAGE source is connected to the system. A RESET signal 240 is provided. A PULSE DETECT 230 is connected to the PULSE COUNTER 230. The system is powered by IG SW and P. S. inputs. A switch 158 is connected to the COMMON terminal 777.

**FIG. 8**

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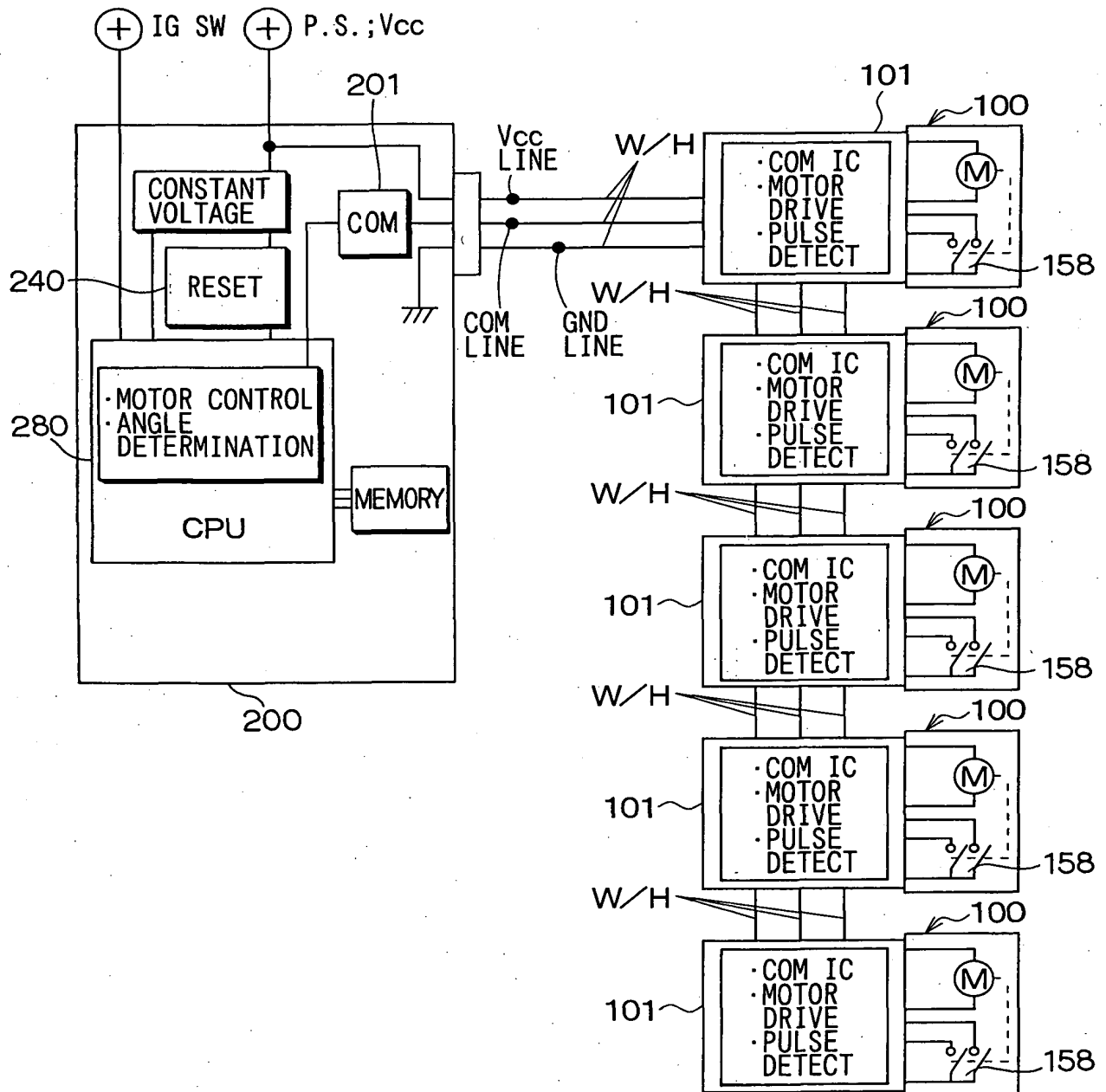


**FIG. 9**



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FIG. 10



**FIG. 11**

